PATENT APPLICATION

/d/A
Smc
5/16/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

TANIGUCHI et al.

Application No.: 09/266,869

Filed: March 12, 1999

For: MICROCONTROLLER HAVING PREFETCH FUNCTION

Art Unit: 2183

Examiner: Gautam R. Patel

Docket No.: 108075-09008

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Technology Center 2600

AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

May 8, 2002

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Please amend the application as follows:

IN THE CLAIMS:

Please amend claims 1, 8, 14 and 16 as follows:

A¹ 540 1. (Amended) A method for prefetching instructions and data of the program
Ab B¹

stored in a memory, wherein the program includes a pseudo instruction and at least one

of an unconditional branch instruction, a conditional branch instruction, a CALL

instruction, and a data calling instruction, the pseudo instruction being arranged before

the at least one instruction and indicating that the at least one instruction or data follows

the pseudo instruction, at least one instruction address or data address being part of the

pseudo instruction, wherein the pseudo instruction is not executed, the method

comprising the steps of: